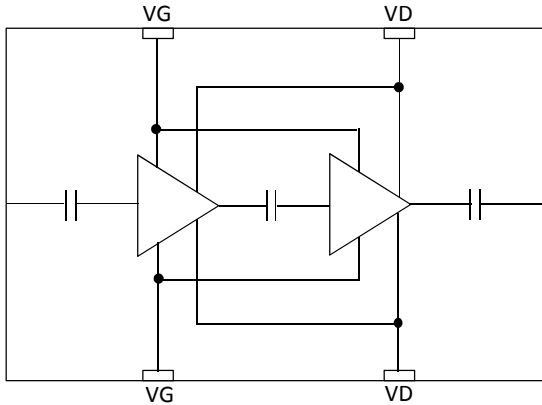


MECX10W-2

X-Band GaAs pHEMT High Power Amplifier



MICROWAVE ELECTRONICS FOR COMMUNICATIONS



Product Description

MECX10W-2 is a 0.25 μ m GaAs pHEMT based High Power Amplifier designed by MEC for X-Band applications.

The MECX10W-2 provides more than 11W of saturated output power in the frequency range from 8.5 GHz to 10.8 GHz, with PAE up to 43% and 21 dB of small signal Gain.

The MECX10W-2 is fully matched to 50 Ω with DC decoupling capacitors on both Input and Output ports. Bond Pad are gold plated for compatibility with thermo-compression bonding process.

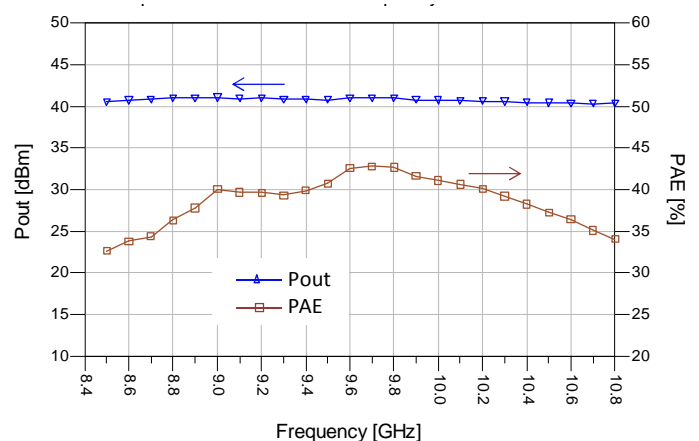
Main Features

- 0.25 μ m GaAs pHEMT Technology
- 8.5 – 10.8 GHz full performances Frequency Range
- Saturated Output Power > 11W
- PAE = 32% - 43%
- Small Signal Gain > 21 dB
- Bias: Vd = 8.5V, Id = 2.6A, Vg = -0.43V (Typ.)
- Chip Size: 5 x 3.3 x 0.07 mm

Primary Applications

- Radar
- Telecom

Measured Data



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Main Characteristics

Test Conditions: $T_{\text{base_plate}} = 20^{\circ}\text{C}$, $V_d = 8.5 \text{ V}$, $I_{dq} = 2.6 \text{ A}$, Pulse Width = 100 μs , Duty Cycle = 30%

Parameter	Min	Typ	Max	Unit
Operating frequency	8.5		10.8	GHz
Small Signal Gain		23.3		dB
Input Return Loss		-14		dB
Output Return Loss		-19		dB
Saturated Output Power		41		dBm
Power Added Efficiency @ $P_{\text{out}}=P_{\text{sat}}$	32		43	%
Gain @ $P_{\text{out}}=P_{\text{sat}}$		18.8		dB
Drain Supply Voltage		8.5		V
Supply Quiescent Drain Current		2.6		A
Supply Drain Current	3.2		4	A
P_{sat} Vs. Temperature		-0.007		dB/ $^{\circ}\text{C}$
PAE @ P_{sat} Vs. Temperature		-0.03		%/ $^{\circ}\text{C}$
Drain Current @ P_{sat} Vs. Temperature		-0.004		A/ $^{\circ}\text{C}$
Linear Gain Vs. Temperature		-0.028		dB/ $^{\circ}\text{C}$

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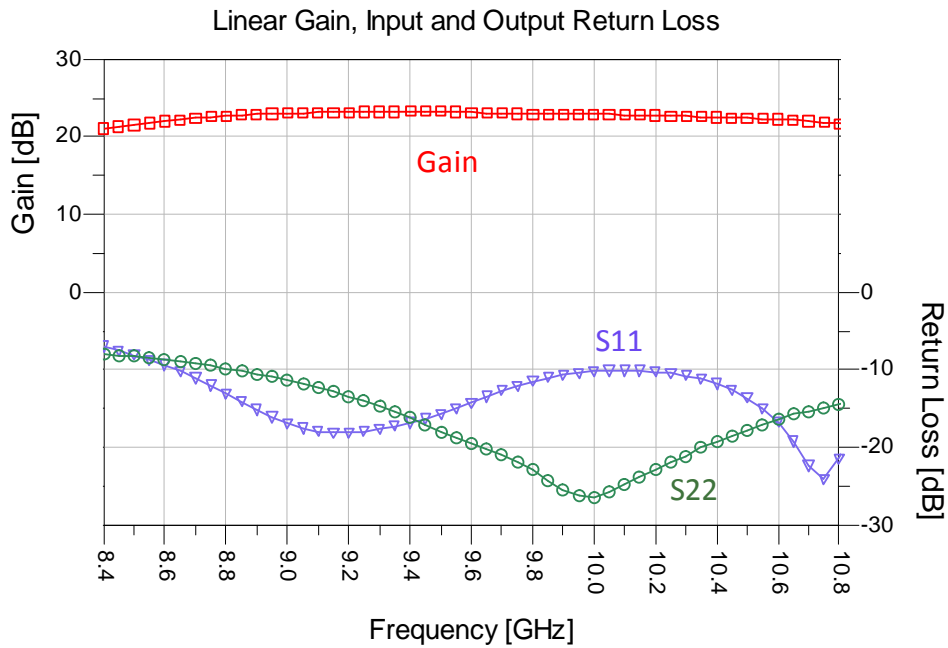
Absolute Maximum Rating*		
Parameter	Values	Unit
Compression Level	6	dB
Drain Supply Voltage with RF input Power	9.0	V
Drain Supply Voltage without RF input Power	10	V
Supply Quiescent Drain Current	3.5	A
Max. forward gate current	12	mA
Max. negative gate source voltage	-2.5	V
Max. negative gate drain voltage	-10	V
Maximum junction temperature	175	°C

* Tamb = 20°C

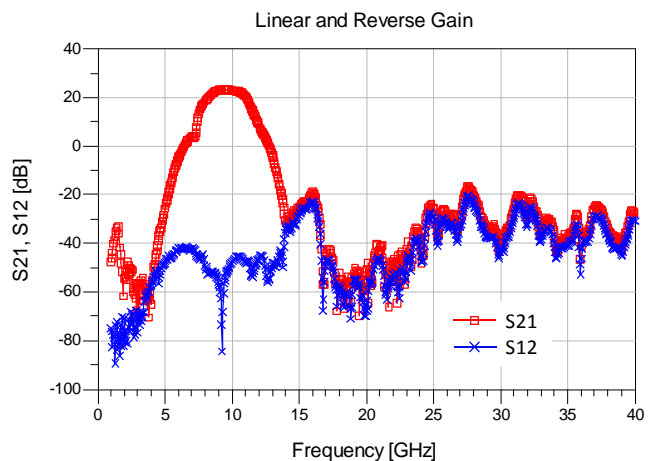
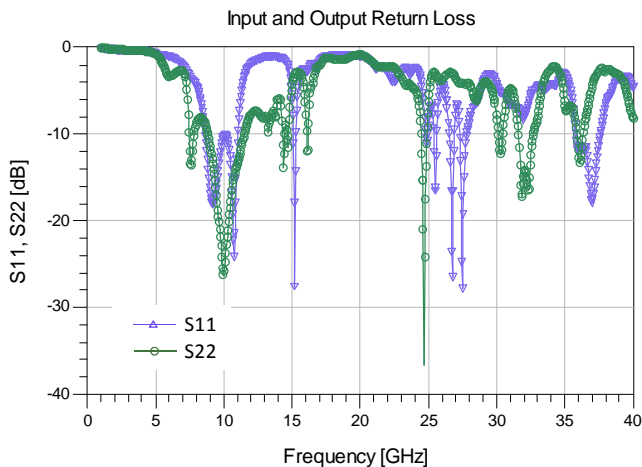
Thermal and Reliability Information*			
Test Conditions	Parameter	Values	Unit
VD = 8.5 V, ID = 2.6 A PDC = 22W, No RF Input Tbaseplate = 80°C	Equivalent Thermal Resistance (No RF Drive)	4	°C/W
	Channel Temperature (No RF Drive)	168	°C
	Mean Time Failure (No RF Drive)	3E+5	Hrs
VD = 8.5 V, ID = 3.3 A PDC = 28W, Pout= 41dBm Tbaseplate = 80°C	Thermal Resistance (Under RF Drive)	4	°C/W
	Channel Temperature (Under RF Drive)	142	°C
	Mean Time Failure (Under RF Drive)	2.8E+6	Hrs

*Assumes eutectic attach using 1.5 mil thick 80/20 AuSn mounted to a 20 mil CuMo Carrier Plate.

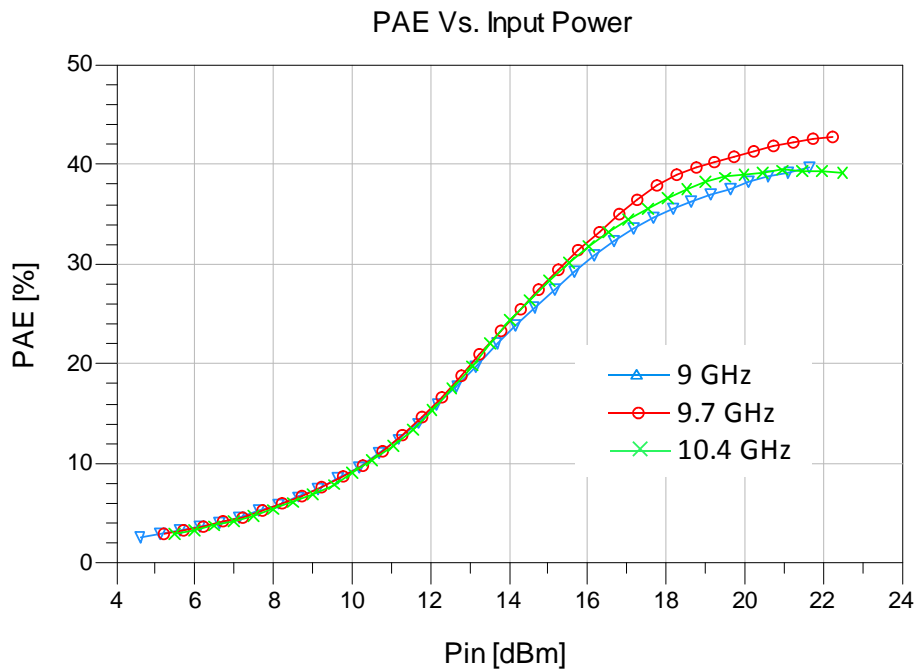
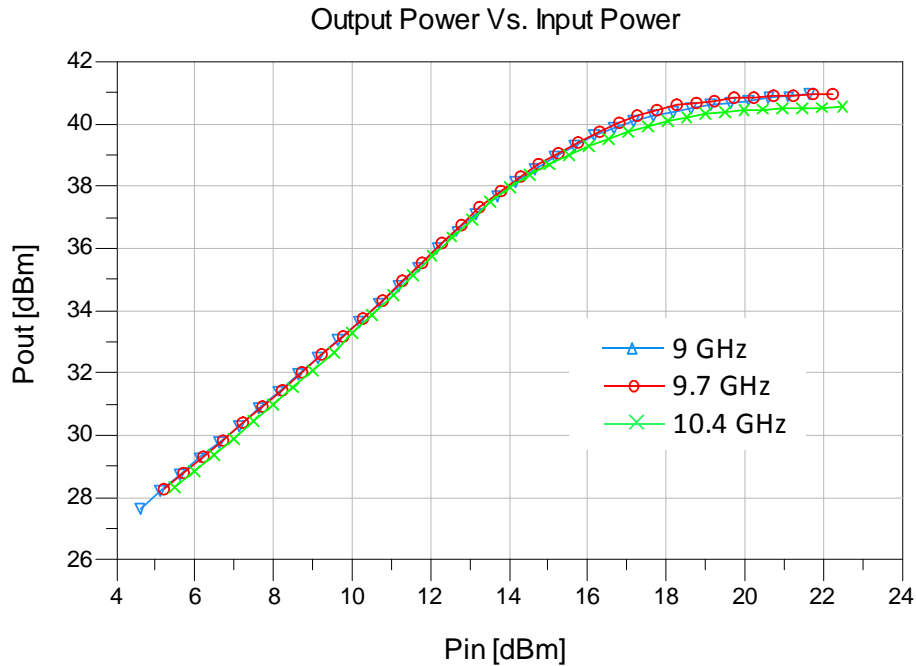
Small Signal Measurements



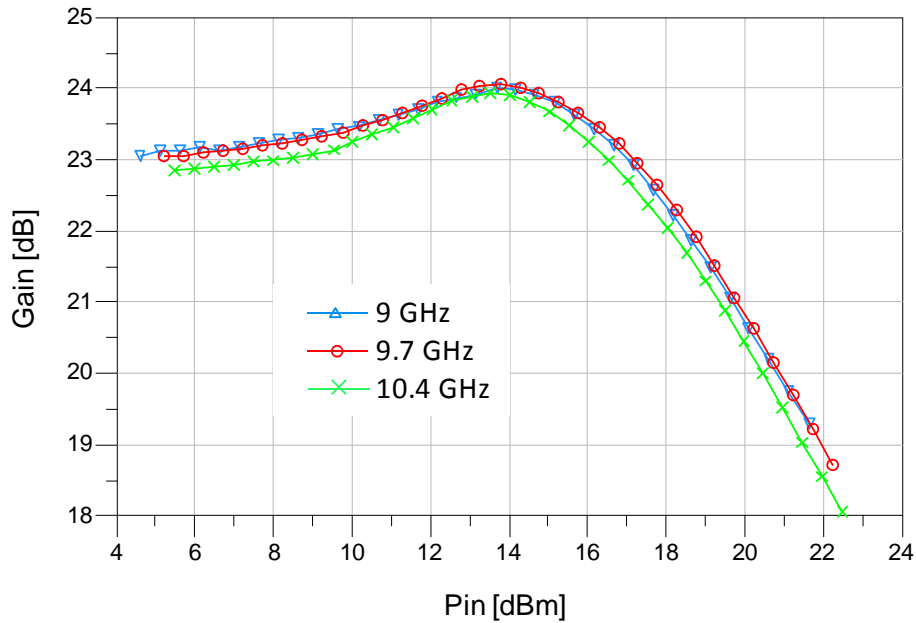
Broadband Small Signal Measurements



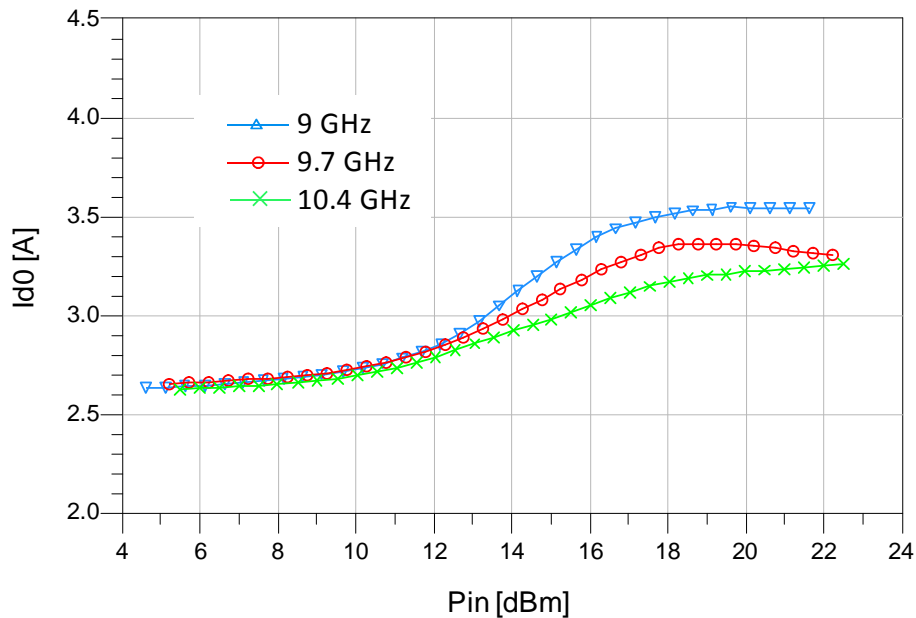
Measured Performances Vs. Pin @ Frequency [9, 9.7, 10.4] GHz



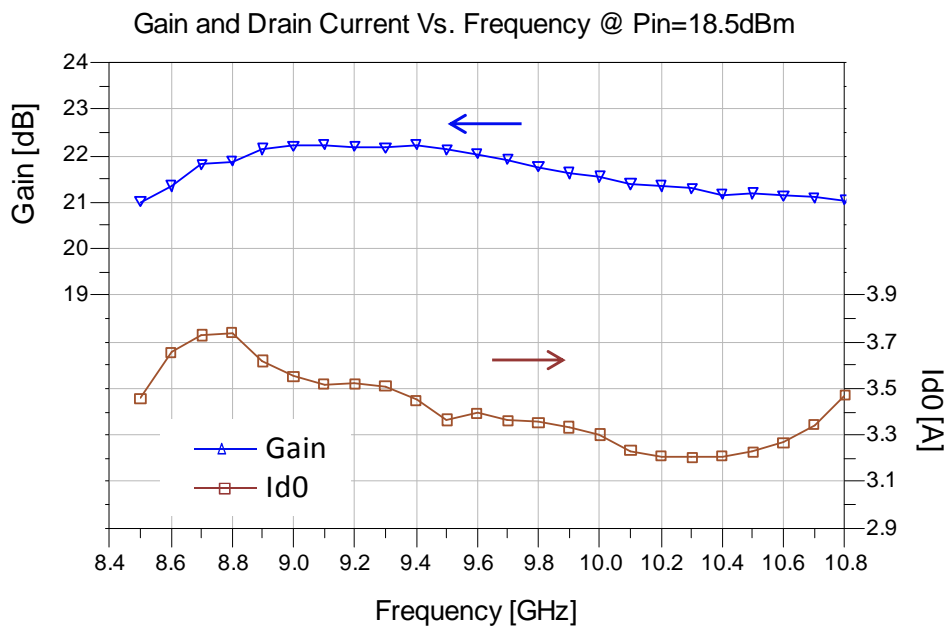
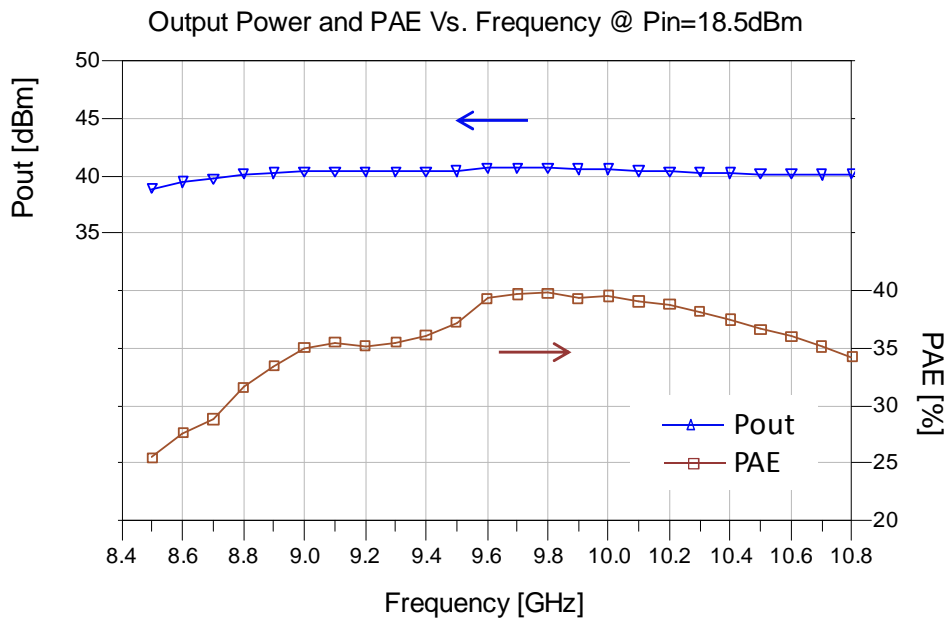
Gain Vs. Input Power



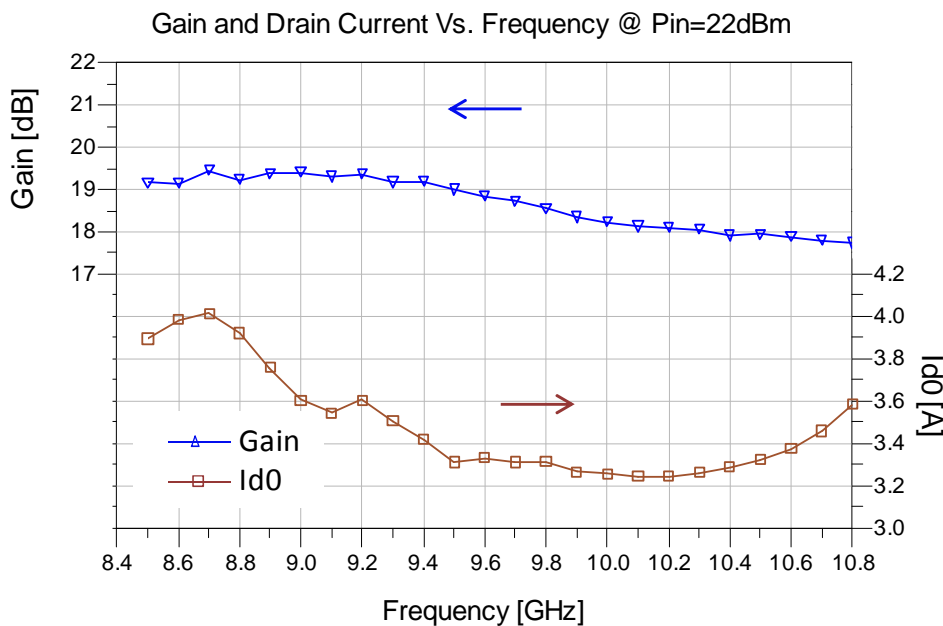
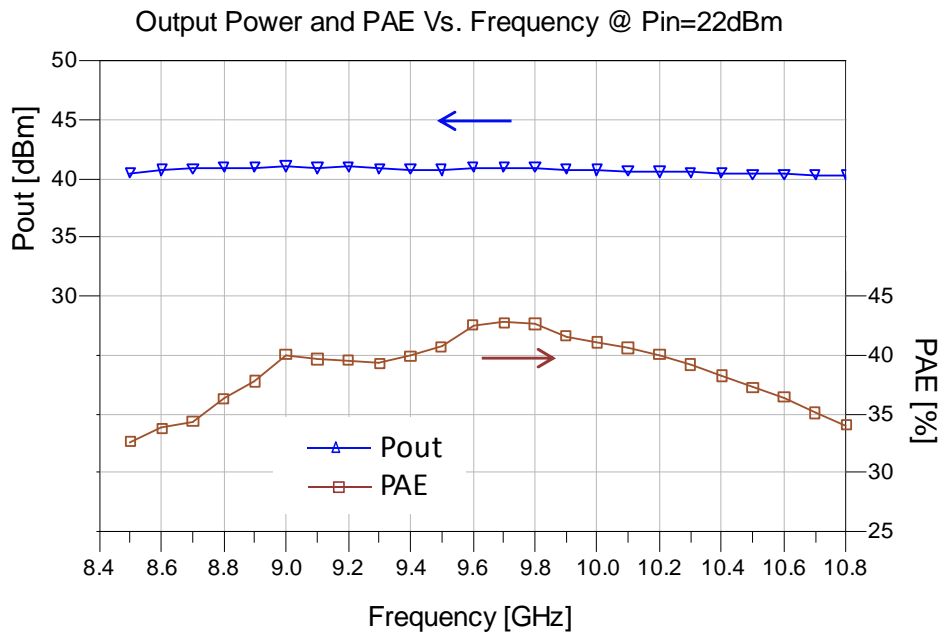
Drain Current Vs. Input Power



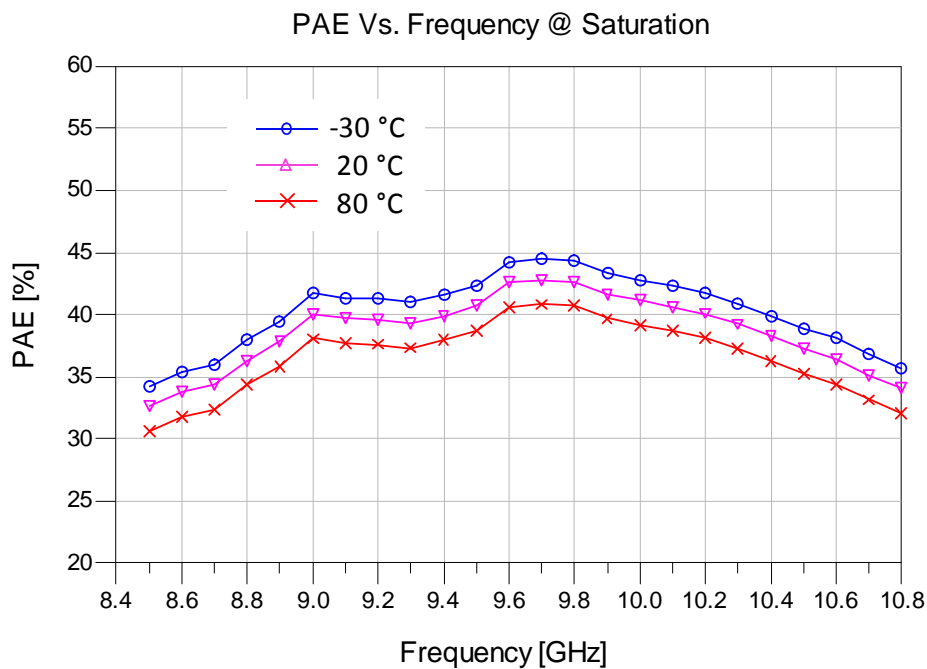
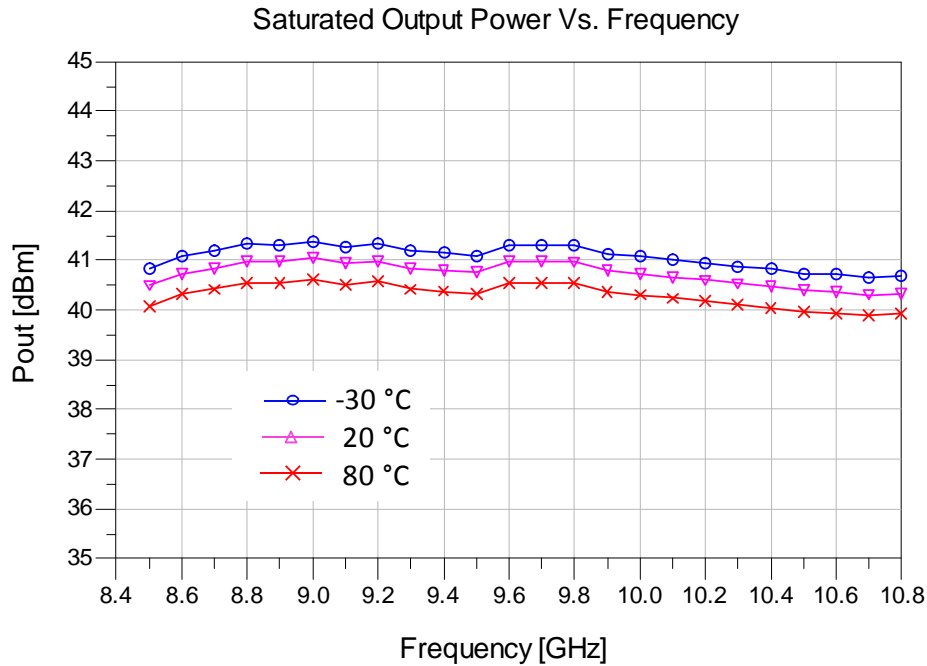
Measured Performances Vs. Frequency @ 1dB of Gain Compression

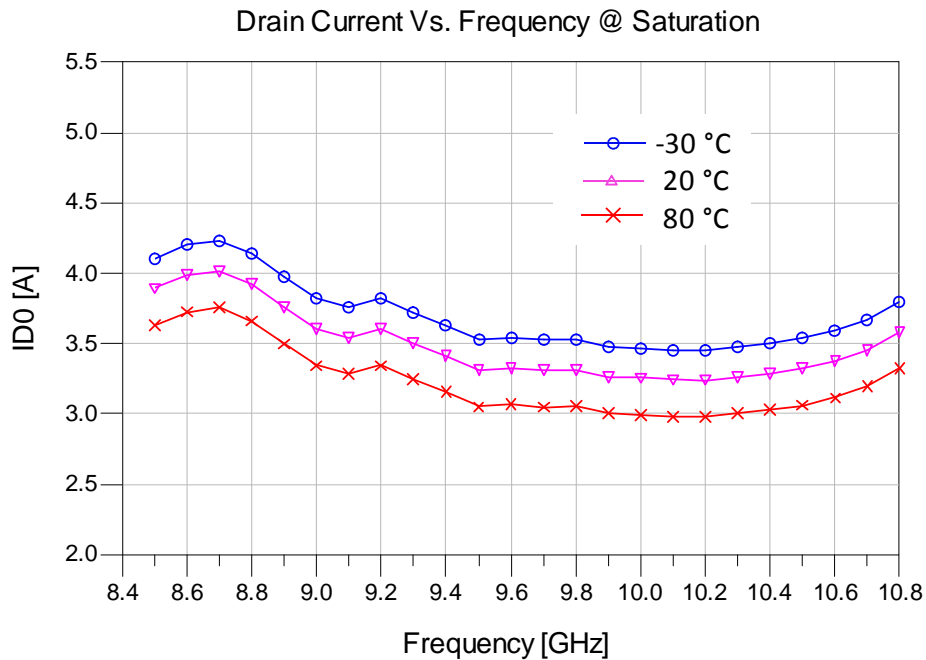
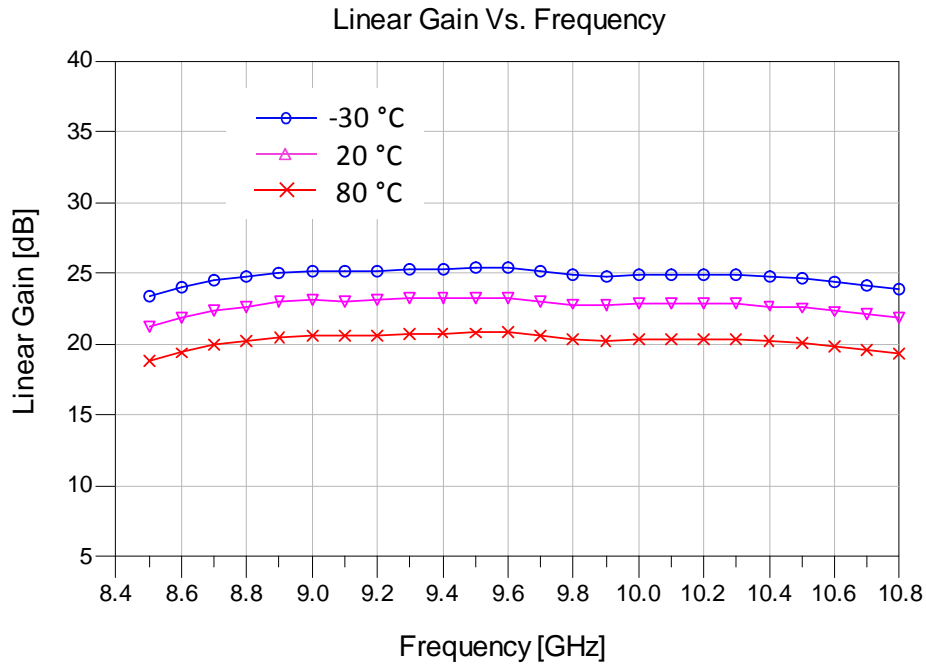


Measured Performances Vs. Frequency @ Saturation

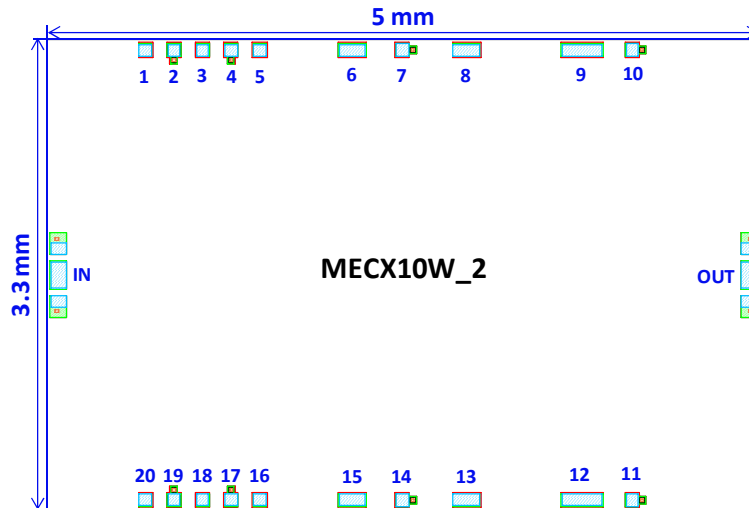


Measured Performances Vs. Frequency @ Temperature [-30, 20, 80]°C





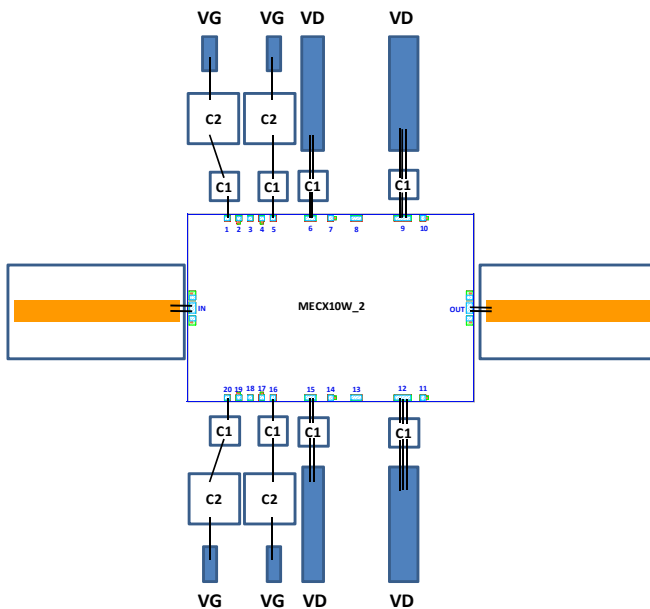
Bond Pad Configuration



- A tolerance of $\pm 35\mu\text{m}$ has to be considered for chip dimensions
- Chip Thickness is $70\mu\text{m} \pm 10\mu\text{m}$
- RF Pads [IN, OUT] = $118\mu\text{m} \times 196\mu\text{m}$
- DC Pads [1, 2, 3, 4, 5, 7, 10, 11, 14, 16, 17, 18, 19, 20] = $100\mu\text{m} \times 100\mu\text{m}$
- DC Pads [6, 8, 13, 15] = $200\mu\text{m} \times 100\mu\text{m}$
- DC Pads [9, 12] = $300\mu\text{m} \times 100\mu\text{m}$

Bond Pad #	Symbol	Description
IN	RFin	Input RF Port
OUT	RFout	Output RF Port
1, 5, 16, 20	Vg	Gate Negative Supply Voltage
6, 9, 12, 15	Vd	Drain Positive Supply Voltage
2, 4, 7, 10, 11, 14, 17, 19	GND	Ground Pads – Not Connected
3, 8, 13, 18	\	Not Connected

Assembly Recommendations



Bond Pad #	Connection	External Components
IN and OUT	2 Bonding Wires $L_{\text{bond}} = 0.3\text{nH}$	
1, 5, 16, 20 - Vg	$L_{\text{bond}} \leq 1\text{ nH}$	C1 = 100pF/10V C2 = 10nF/10V
6, 15 - Vd	2 Bonding Wires $L_{\text{bond}} \leq 1\text{ nH}$	Pulsed mode C1 = 100pF/50V
9, 12 - Vd	3 Bonding Wires $L_{\text{bond}} \leq 1\text{ nH}$	CW mode: C1 = 100pF/50V C2 = 10nF/50V

- Eutectic Die bond using AuSn (80/20) solder is recommended.
- Great care must be used for thermal dimensioning.
- The backside of the die is the Source (ground) contact.
- Thermosonic ball or wedge bonding are the preferred connection methods.
- Gold wire must be used for connections.

Bias Procedure

Bias-Up

1. Vg set to -1.5 V.
2. Vd set to +8.5 V.
3. Adjust Vg until quiescent Id is 2.6 A
(Vg = -0.43 V Typical).
4. Apply RF signal.

Bias-Down

1. Turn off RF signal.
2. Reduce Vg to -1.5 V ($I_{d0} \approx 0\text{ mA}$).
3. Set Vd to 0 V.
4. Turn off Vd.
5. Turn off Vg.

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Notice

The furnished information is believed to be reliable. However, performances and specifications contained herein are based on preliminary characterizations and then susceptible to possible variations. On the basis of customer requirements the product can be tested and characterized in specific operating conditions and, if needed, tuned to meet custom specifications.

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